Page 1 of 3

14

FORM PTO-1449

SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO. 1778.0180000 FIRST NAMED INVENTOR Christopher R. Risucci FILING DATE August 10, 2001 APPLICATION NO. 09/925,314 ART UNIT 2183

EXAMINER			U.S. PA	TENT DOCUMENTS	·	E	
4 5 4 4 TO 4 A 4		DOCUMENT	DATE	NAME	CLASS	SUB- CLASS	PANEARING DATE
INITIAL .	AA1	NUMBER 3,794,980	02/1974	NAME Cogar et al.	CLASS	CLASS	FILING DATE
	AB1	3,811,114	05/1974	Lemay et al.			
	AC1	3,840,861	10/1974	Amdahl et al.		+	
	AD1	3,983,541	09/1976	Faber et al.		1	
	AE1	4,110,822	08/1978	Porter et al.	-	-	
	AF1	4,149,244	04/1979	Anderson et al.			
	 		+				
	AG1	4,229,790	10/1980	Gilliland et al.		 	
	AH1	4,295,193	10/1981	Pomerene, James H.		-	
1 -	Al1	4,432,056	02/1984	Aimura, Harutsugu		·	
2	AJ1	4,467,409	08/1984	Potash et al.			
EXAMINER	1	DOCUMENT	FOREIGN	PATENT DOCUMENTS		SUB-	T
INITIAL	<u> </u>	NUMBER	DATE	COUNTRY	CLASS	CLASS	TRANSLATION
4-9	AK1	EP 0 073 424 A2	03/1983	Europe	0.40	:	N/A
1	AL1	EP 0 239 081 B1	09/1995	Europe			N/A
125	AM1	EP 0 368 332 B1	09/1997	Europe			N/A
		OTHER (In	cluding Auth	or, Title, Date, Pertinent	Pages, etc.)		
45	AN1	Cobb, Paul, "TinyR 13 slides (7 pages)		16 embedded CPU core," (23, 1996).	Presentation	for Micropro	ocessor Forum,
	1	1					
	AO1	U.S. Utility Patent A (not published) (67		o. 09/702,112, inventors Je	nsen, M., et a	al., filed Oct	ober 30, 2000
	AO1	(not published) (67	pages).	o. 09/702,112, inventors Je			
		(not published) (67 U.S. Utility Patent A (not published) (71 U.S. Reissue Pater	Application No pages).		ensen, M., <i>et a</i>	al., filed Oct	ober 30, 2000
Ho)	AP1	(not published) (67 U.S. Utility Patent A (not published) (71 U.S. Reissue Pater 2002 (based on U.S. Preliminary Amend	Application No pages). Application No pages). Application No. 6,0 The Application S. Pat. No. 6,0 The Application Filed Feblies Nevill, filed Feblies	o. 09/702,115, inventors Je No. 10/066,475, inventor E	Edward Collect, 2000) (9 pa	s Nevill, filed	ober 30, 2000 d February 1, No. 10/066,475,
EXAMINER	AP1	(not published) (67 U.S. Utility Patent A (not published) (71 U.S. Reissue Pater 2002 (based on U.S. Preliminary Amend inventor Edward Co	Application No pages). Application No pages). Application No. 6,0 The Application S. Pat. No. 6,0 The Application Filed Feblies Nevill, filed Feblies	No. 10/066,475, inventors Je 221,265, issued February	Edward Collect 1, 2000) (9 parties on U.S. Parties	s Nevill, filed	ober 30, 2000 d February 1, No. 10/066,475, ,265, issued

	TPE			
	/ 0 ~ 3	\	Page :	2 of 3
	8	ATTY. DOCKET NO.	APPLICATION NO.	
FORM PTO-1449	3 D 2005 G	1778.0180000	09/925,314	
	JUH 3 0 MM	FIRST NAMED INVENTOR		
SECOND SUPPLEMEN	par 📡	Christopher R. Risucci		
INFORMATION DISCLOSURE S	THE MENT	FILING DATE	ART UNIT	
IN ONWATION DISOLOGGINE C		August 10, 2001	2183	

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
12	AA2	4,507,728	03/1985	Sakamoto et al.			
1/	AB2	4,685,080	08/1987	Rhodes, Jr. et al.			
	AC2	4,724,517	02/1988	May, Michael D.			
	AD2	4,777,594	10/1988	Jones et al.			
	AE2	4,782,441	11/1988	Inagami et al.			
	AF2	5,132,898	07/1992	Sakamura et al.			
.	AG2	5,241,636	08/1993	Kohn, Leslie D.			
	AH2	5,327,566	07/1994	Forsyth, Mark A.			
	Al2	5,355,460	10/1994	Eickemeyer et al.			
15	AJ2	5,506,974	04/1996	Church et al.			
	.1		FOREIGN	PATENT DOCUMENTS			
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
12-5)	AK2	EP 0 449 661 B1	11/1995	Europe			N/A
,,,	AL2						Yes
	AM2					-	No Yes
	<u> </u>	OTHER (In	cluding Auth	or, Title, Date, Pertinent	Pages etc.)		No_
12-57	AN2	Gwennap, Linley, " Cheaper Than RIS	VLIW: The W C," <i>Microproc</i>	lave of the Future?: Processor Report, Vol. 8, No.	essor Design 2, pp. 18-21 (I	Style Could February 14	l Be Faster, 1, 1994).
	AO2	Kurosawa, K., et al., "Instruction Architecture For A High Performance Integrated Prolog Processor IPP," Logic Programming: Proceedings of the Fifth International Conference and Symposium (August 15-19, 1988), MIT Press, Cambridge, MA, Vol. 2, pp. 1506-1530 (1988).					
	AP2	IBM Technical Disclosure Bulletin, "Patchable Read-Only Storage and Other Patchable Functions," Vol. 27, Issue 6, pp. 3496-3499 (November 1, 1984) (4 pages).					
	AQ2	IBM Technical Disclosure Bulletin, "Patch RAM Load Technique," Vol. 27, Issue 6, pp. 3597-3598 (November 1, 1984) (3 pages).					
12-5	AR2	IBM Technical Disc (June 1, 1978) (3 p		n, "Microcode Memory Ch	nanges," Vol. 2	21, Issue 1,	pp. 341-342
EXAMINER		Verm.	/	•	DATE	CONSIDERE	D /0 (-
		erence considered, whether		s in conformance with MPEP 60	09. Draw line thro	ugh ditation if	- / - 3

FORM PTO-1449

SECOND SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.
1778.0180000
FIRST NAMED INVENTOR
Christopher R. Risucci
FILING DATE

August 10, 2001

APPLICATION NO. 09/925,314

ART UNIT

2183

JUN 3 0 2005

Page 3 of 3

U.S. PATENT DOCUMENTS FXAMINER DOCUMENT SUB-DATE **CLASS FILING DATE** NAME **CLASS** INITIAL NUMBER AA3 5,574,873 11/1996 Davidian, Gary G. 03/1998 AB3 Vassiliadis et al. 5,732,234 AC3 6,266,765 B1 07/2001 Horst, Robert W. 6,272,620 B1 AD3 08/2001 Kawasaki et al. 09/2001 AE3 2001/0021970 A1 Hotta et al. 2004/0054872 A1 03/2004 AF3 Nguyen et al. AG3 AH3 AI3 AJ3 **FOREIGN PATENT DOCUMENTS** EXAMINER DOCUMENT SUB-CLASS NUMBER **TRANSLATION** INITIAL DATE COUNTRY **CLASS** Yes AK3 No Yes AL3 No Yes AM3 No OTHER (Including Author, Title, Date, Pertinent Pages, etc.) NEC Data Sheet, MOS Integrated Circuit, uPD30121, VR4121 64-/32-Bit Microprocessor (Copyright AN3 NEC Electronics Corporation 2000) (76 pages). Ross, Roger, "There's no risk in the future for RISC," Computer Design, Vol. 28, No. 22, pp. 73-75 AO3 (November 13, 1989). NEC User's Manual, VR4100 Series[™], 64-/32-Bit Microprocessor Architecture, pp. 1-11 and 54-83 AP3 (Chapter 3) (Copyright NEC Corporation 2002). AQ3 AR3 **EXAMINER** DATE CONSIDERED 0 EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

Page 1 of 1

FORM PTO-1449

THIRD SUPPLEMENTA

INFORMATION DISCLOSURE STATE FALLER

ATTY. DOCKET NO.

APPLICATION NO.

09/925,314

FIRST NAMED INVENTOR

Christopher R. Risucci

FILING DATE
August 10, 2001

ART UNIT
2183

. . . .

			U.S. P	ATENT DOCUMENTS	S		
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB- CLASS	FILING DATE
	AA1						
	AB1		•				
	AC1						
	AD1						
	AE1	-					
-	AF1						
	AG1						
	AH1						
	Al1						
	AJ1						
	AK1					<u> </u>	
			FOREIGN	PATENT DOCUME	NTS		
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION
	AL1						Yes No
•	AM1						Yes
	AN1						No Yes
	AO1						No Yes
	 						No Yes
	AP1	07:150 (1	<u> </u>				No
		OTHER (In	cluding Aut	hor, Title, Date, Pert	inent Pages, etc.)		
4	AR1	Case, Brian, "ARM Options in Each In 1991).	Architecture struction," M	Offers High Code Delicroprocessor Report	ensity: Non-Tradition , Vol. 5, No. 23, pgs	nal RISC E s. 11-14 (De	ncodes Many ecember 18,
	AS1						
	AT1	, 1	\neg	7			
EXAMINER		/ Oc v	1 /1	3	DATE (CONSIDERE	08/12/00
EXAMINER: In	itial if reference	erence considered, wheth	er or not citation	is in conformance with Milation to Applicant.	PEP 609. Draw line thro	ugh citation if	not in conformance

426104_1.DOC